



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant(s): Erich Franz Haratsch,
Harish Viswanathan

Case: 1-4

Serial No.: 09/326,785

Filing Date: June 4, 1999

Title: Method and Apparatus for Reducing the Computational Complexity and Relaxing
the Critical Path of Reduced State Sequence Estimation (RSSE) Techniques

Group: 2776

I hereby certify that this paper is being deposited on this date with
the United States Postal Service as first class mail addressed to the
Assistant Commissioner for Patents, Washington, D.C. 20731

Date of Deposit: March 2, 2001

Signature: Linda M. Shockley
Technology Center 2000

GP/2776
2176
#16
Haratsch
Chg of address
Phyllis S. Haratsch
03/16/01
RECEIVED
MAR 16 2001

CHANGE OF CORRESPONDENCE ADDRESS

Assistant Commissioner for Patents
Washington, D.C. 20231

SIR:

In accordance with the Manual of Patent Examining Procedure, Section 601.08,
please change the mailing address and firm name for all correspondence in the above-identified
application to:

Kevin M. Mason
Ryan, Mason & Lewis, LLP
1300 Post Road, Suite 205
Fairfield, CT 06430

Respectfully submitted,

RYAN, MASON & LEWIS, LLP

Date: March 2, 2001

By: Kevin M. Mason
Kevin M. Mason
Reg. No. 36,597

Mailing Address:
Ryan, Mason & Lewis, LLP
1300 Post Road, Suite 205
Fairfield, CT 06430
TEL: (203) 255-6560
FAX: (203) 255-6570

RECEIVED
MAR 09 2001
Technology Center 2000